

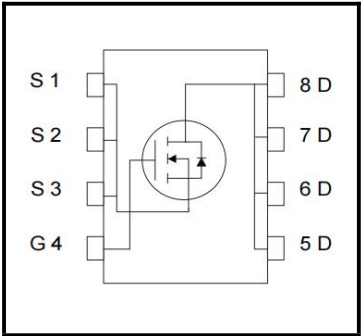
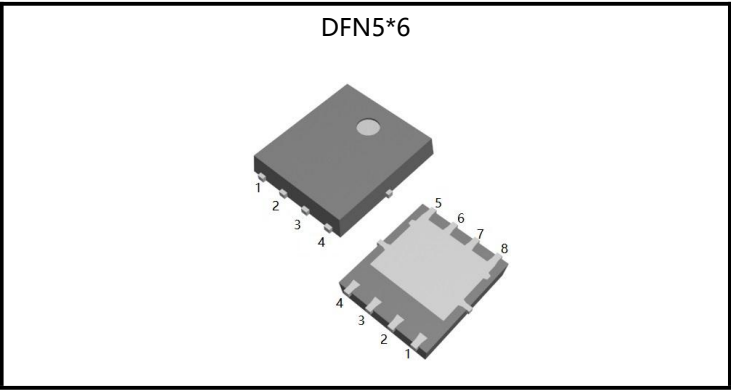


MOSFET

7 Amps,650 Volts N-Channel Super Junction Power MOSFET

FEATURE

- ◆ Low gate charge
- ◆ Low Ciss
- ◆ Fast switching
- ◆ 100% avalanche tested
- ◆ Improved dv/dt capability
- ◆ RoHS 2.0 Compliant



Parameter	Values	Unit
Bvdss	650	V
Id	7	A
Rdson(max)	0.6	Ω

Ordering Code	Marking	Package	Packaging
PWE600N65ES	E600N65ES	DFN5*6	Tape and reel

Absolute Maximum Ratings($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Voltage	V_{DSS}	-	-	650	V	-
Gate-Source Voltage	V_{GS}	-20	-	20	V	-
Continuous Drain Current(Package Limited)	I_D	-	-	7	A	$T_C=25^{\circ}\text{C}$
		-	-	4.2	A	$T_C=100^{\circ}\text{C}$
Pulsed Drain Current(Note1)	I_{DM}	-	-	28	A	-
Single Pulse Avalanche Energy	E_{AS}	-	-	52	mJ	$L=10\text{mH}, V_D=50\text{V}, T_C=25^{\circ}\text{C}$
Maximum Power Dissipation	P_D	-	-	48	W	$T_C=25^{\circ}\text{C}$
Reverse Diode dV/dt (Note2)	dv/dt	-	-	15	V/ns	-
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55	-	150	$^{\circ}\text{C}$	-
Maximum lead temperature for soldering purposes, 1/8"from case for 5 seconds	T_L	-	-	260	$^{\circ}\text{C}$	-

Thermal Characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Thermal resistance , Channel to Case	$R_{th(ch-c)}$	-	2.6	-	$^{\circ}\text{C}/\text{W}$	-
Thermal resistance , Channel to Ambient	$R_{th(ch-a)}$	-	62.5	-		-

Electrical Characteristics ($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Static characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Breakdown Voltage	BV_{DSS}	650	-	-	V	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	$V_{DS}=650\text{V}, V_{GS}=0\text{V}$
Gate-Body Leakage Current, Forward	I_{GSSF}	-	-	100	nA	$V_{GS}=20\text{V}, V_{DS}=0\text{V}$
Gate-Body Leakage Current, Reverse	I_{GSSR}	-	-	-100	nA	$V_{GS}=-20\text{V}, V_{DS}=0\text{V}$
Gate-Source Threshold Voltage	$V_{GS(th)}$	2.0	-	4.0	V	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$
Drain-Source On-State Resistance	$R_{DS(on)}$	-	0.5	0.6	Ω	$V_{GS}=10\text{V}, I_D=3.5\text{A}$
Gate Resistance	R_g	-	13	-	Ω	$V_{GS}=0\text{V}, V_{DS}$ Open, $f=1\text{MHz}$
Forward Transconductance	g_{fs}	-	3.3	-	S	$V_{DS}=5\text{V}, I_D=3.5\text{A}$

Dynamic characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Input Capacitance	C_{iss}	-	466	-	pF	$V_{DS}=25V, V_{GS}=0V, f=1.0MHz$
Output Capacitance	C_{oss}	-	395	-	pF	$V_{DS}=25V, V_{GS}=0V, f=1.0MHz$
Reverse Transfer Capacitance	C_{rss}	-	50	-	pF	$V_{DS}=25V, V_{GS}=0V, f=1.0MHz$
Turn-On Delay Time	$t_{d(on)}$	-	12.4	-	ns	$V_{DD}=325V, R_G=10\Omega, V_{GS}=10V, R_L=150\Omega$
Turn-On Rise Time	t_r	-	2.0	-	ns	$V_{DD}=325V, R_G=10\Omega, V_{GS}=10V, R_L=150\Omega$
Turn-Off Delay Time	$t_{d(off)}$	-	56.4	-	ns	$V_{DD}=325V, R_G=10\Omega, V_{GS}=10V, R_L=150\Omega$
Turn-Off Fall Time	t_f	-	11.2	-	ns	$V_{DD}=325V, R_G=10\Omega, V_{GS}=10V, R_L=150\Omega$

Gate charge characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Total Gate Charge	Q_g	-	19.7	-	nC	$V_{DS}=520V, I_D=7A, V_{GS}=10V$
Gate-Source Charge	Q_{gs}	-	4.1	-	nC	$V_{DS}=520V, I_D=7A, V_{GS}=10V$
Gate-Drain Charge	Q_{gd}	-	7.0	-	nC	$V_{DS}=520V, I_D=7A, V_{GS}=10V$

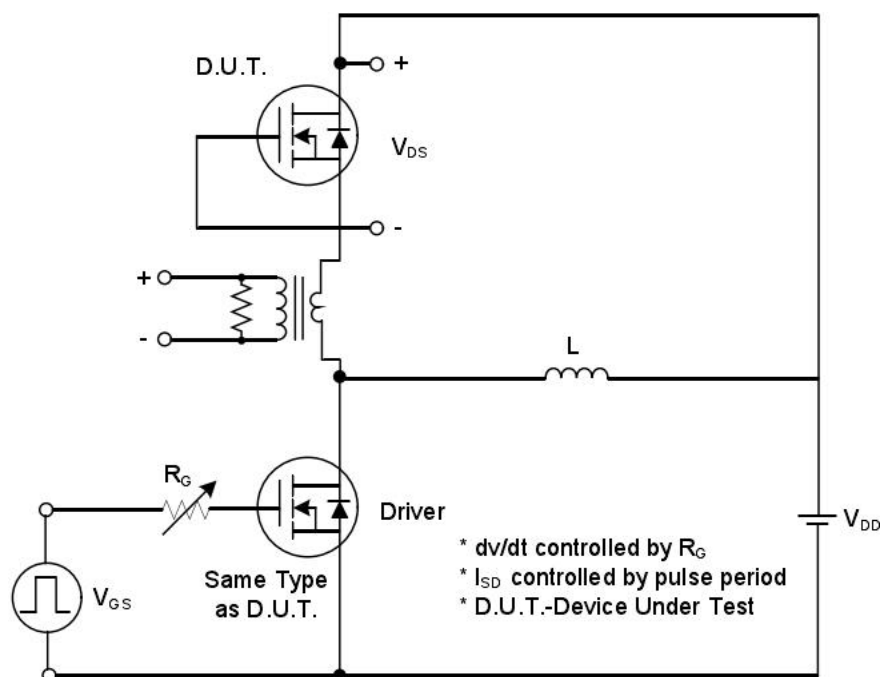
Reverse diode

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Continuous Diode Forward Current	I_S	-	-	7	A	-
Pulsed Diode Forward Current	I_{SM}	-	-	28	A	-
Diode Forward Voltage	V_{SD}	-	-	1.5	V	$I_S=3.5A, V_{GS}=0V$
Reverse Recovery Time	t_{rr}	-	151	-	ns	$V_D=30V, I_F=1A$
Reverse Recovery Charge	Q_{rr}	-	565.7	-	nC	$di/dt=100A/\mu s, (Note2)$

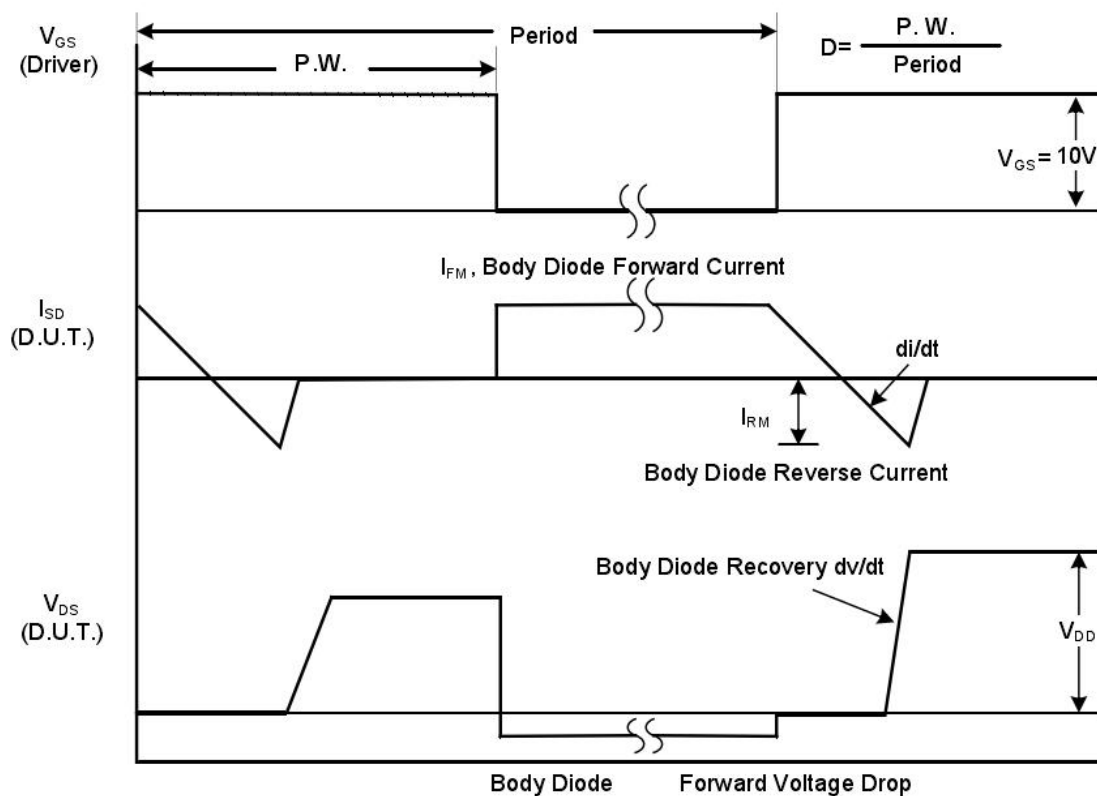
Notes

1. Repetitive Rating:pulse width limited by maximum junction temperature.
2. Pulse width $\leq 300\mu s$,duty cycle $\leq 2\%$.

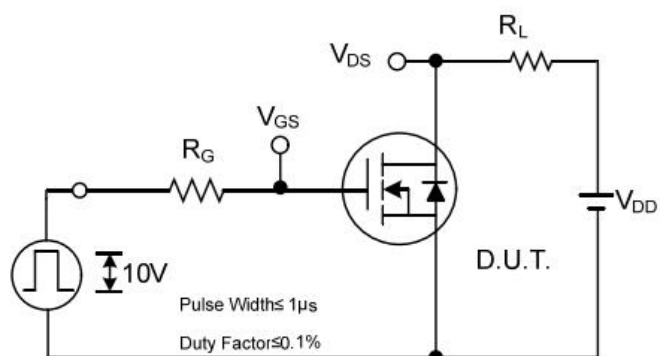
RATING AND CHARACTERISTIC CURVES



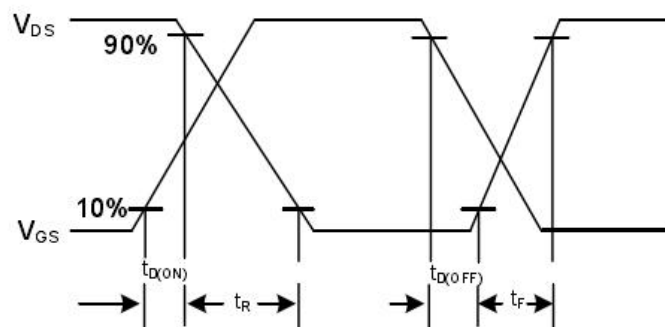
Peak Diode Recovery dv/dt Test Circuit



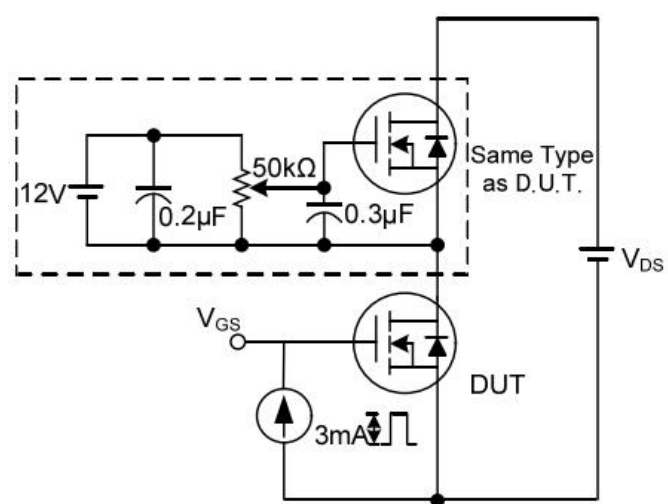
Peak Diode Recovery dv/dt Waveforms



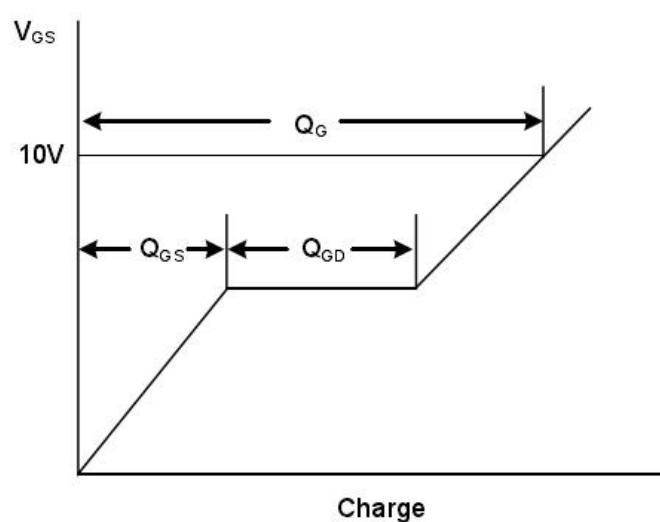
Switching Test Circuit



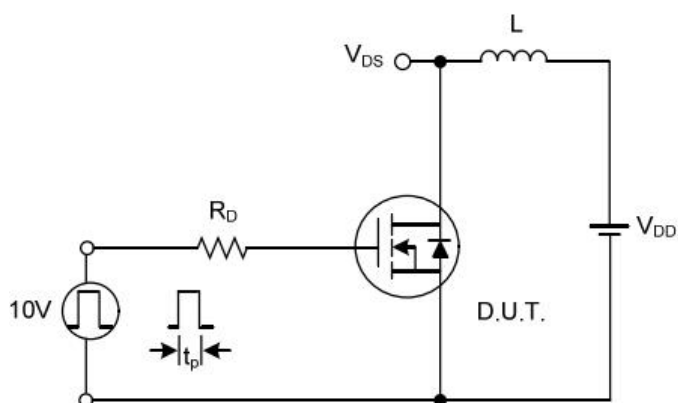
Switching Waveforms



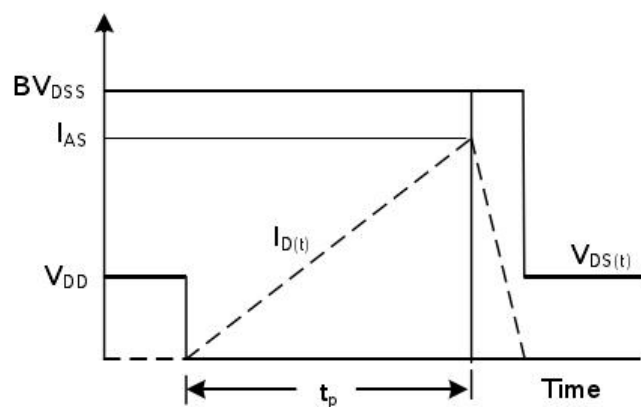
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms



RATING AND CHARACTERISTIC CURVES

Figure.1 Typical Output Characteristics

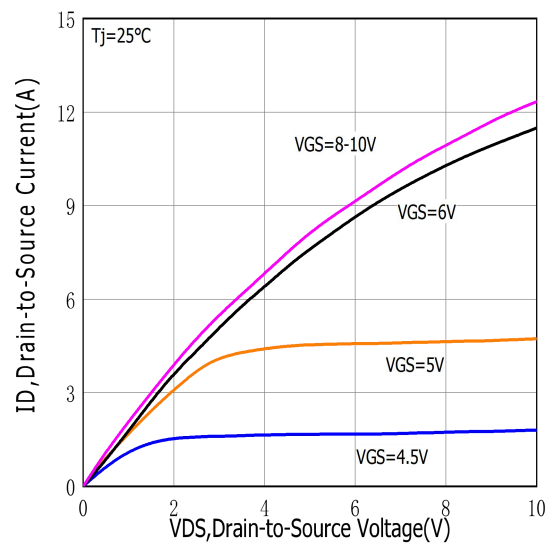


Figure.2 Typical Gate Charge vs Gate to Source Voltage

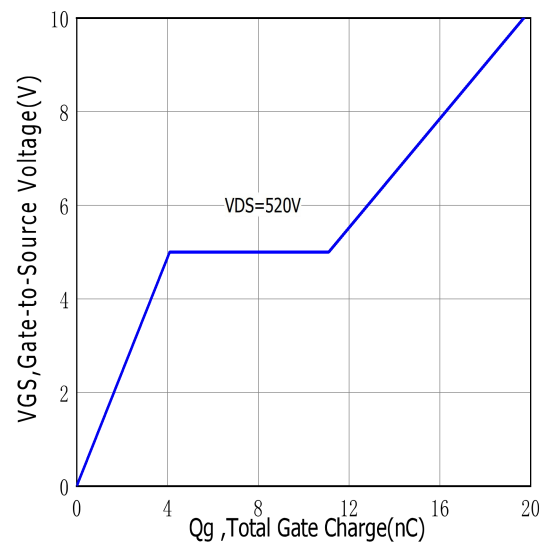


Figure.3 Typical Body Diode Transfer Characteristics

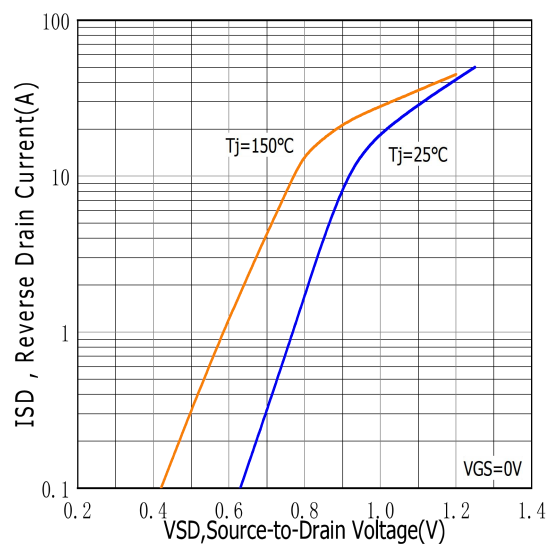


Figure.4 Typical Capacitance vs Drain to Source Voltage

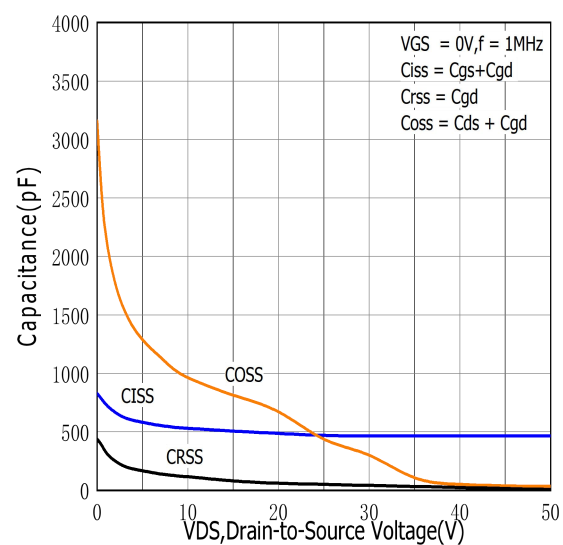




Figure.5 Typical Breakdown Voltage vs Junction Temperature

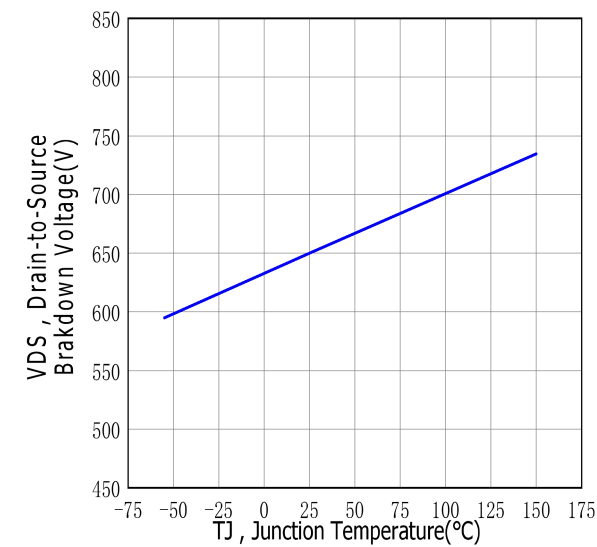


Figure.6 Typical Drain to Source on Resistance vs Junction Temperature

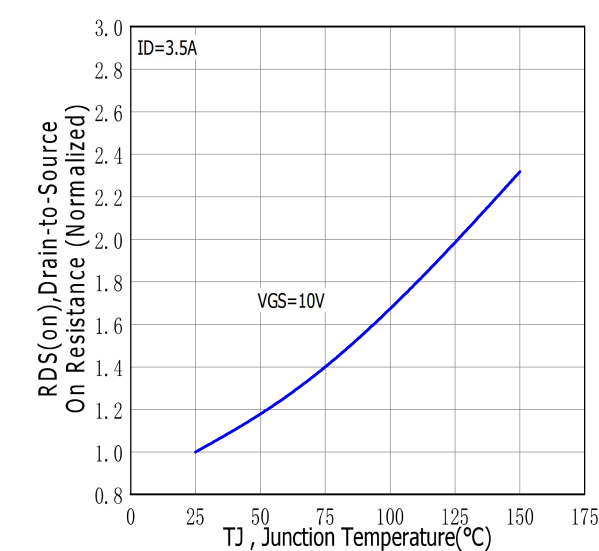


Figure.7 Maximum Forward Bias Safe Operating Area

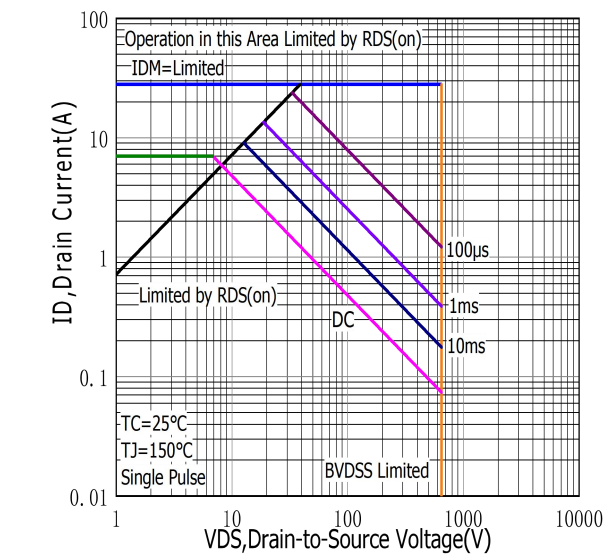


Figure.8 Typical Drain to Source ON Resistance vs Drain Current

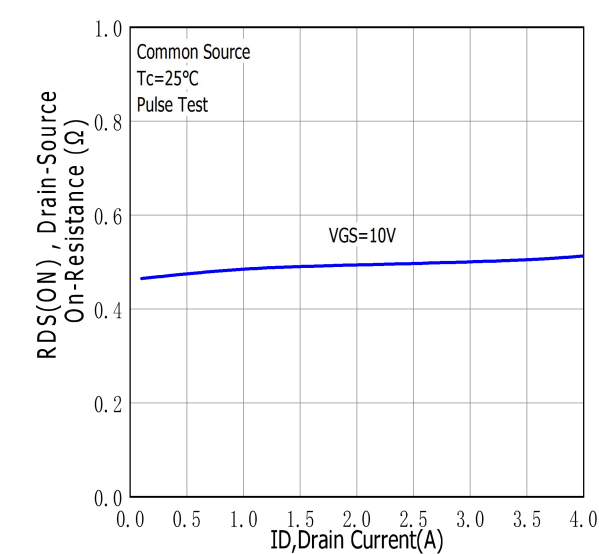




Figure.9 Maximum EAS vs Channel Temperature

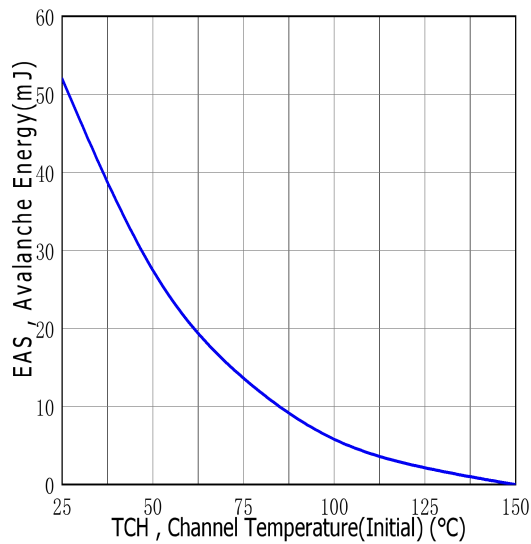


Figure.10 Typical Threshold Voltage vs Case Temperature

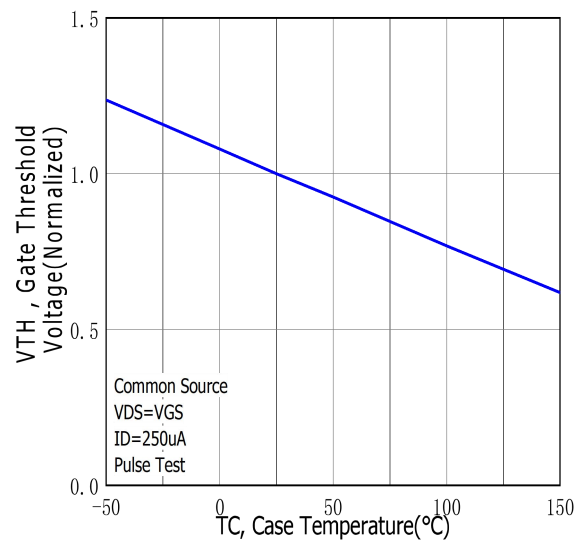


Figure.11 Typical Transfer Characteristics

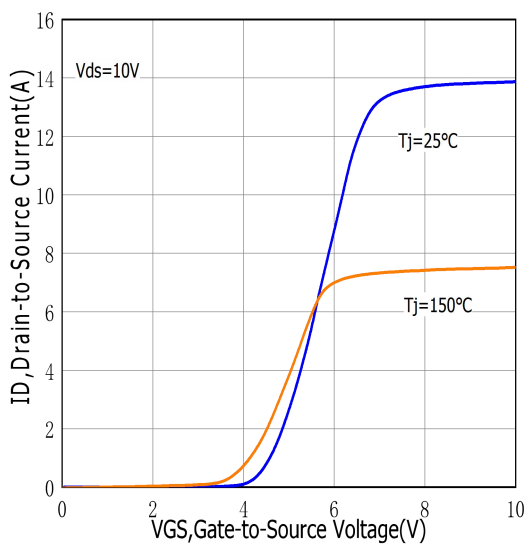


Figure.12 Maximum Power Dissipation vs Case Temperature

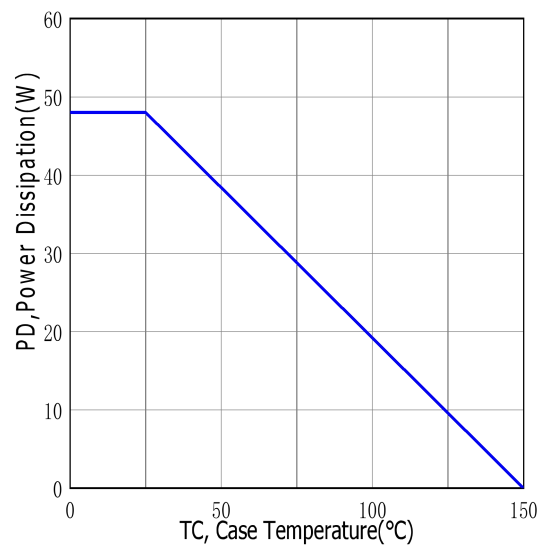
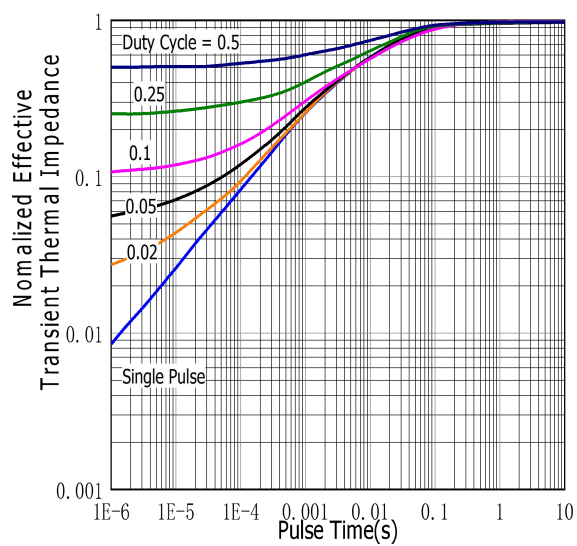
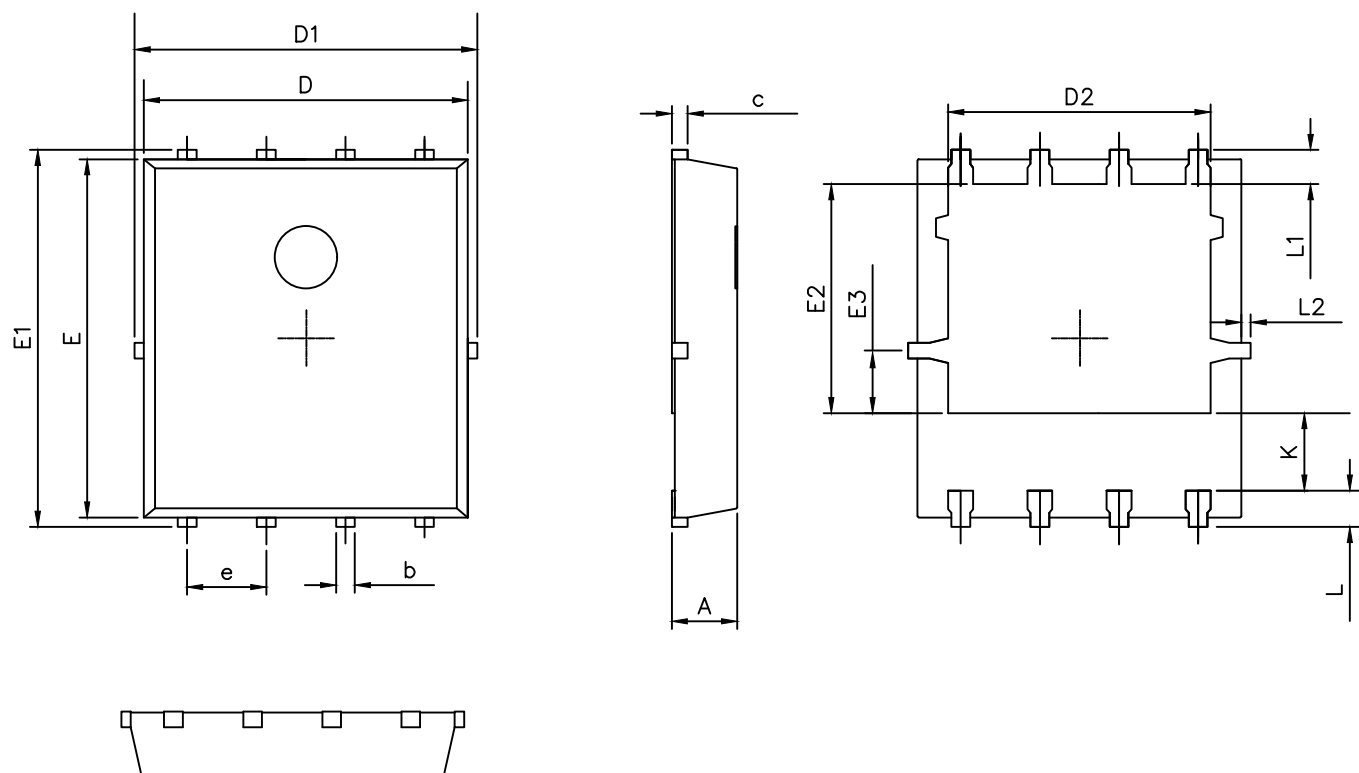


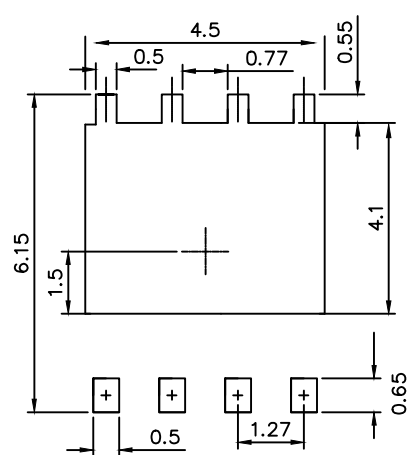
Figure.13 Maximum Effective Thermal Impedance , Junction to Case



DFN5x6 PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



UNIT: mm

	MIN	NOM	MAX
A	0.90	1.00	1.10
b	0.25	0.35	0.50
c	0.10	0.20	0.30
D	4.80	5.00	5.30
D1	4.90	5.10	5.50
D2	3.92	4.02	4.20
E	5.65	5.75	5.85
E1	5.90	6.05	6.20
E2	3.325	3.525	3.775
E3	0.80	0.90	1.00
e		1.27	
L	0.40	0.55	0.70
L1		0.65	
L2	0.00		0.15
K	1.00	1.30	1.50